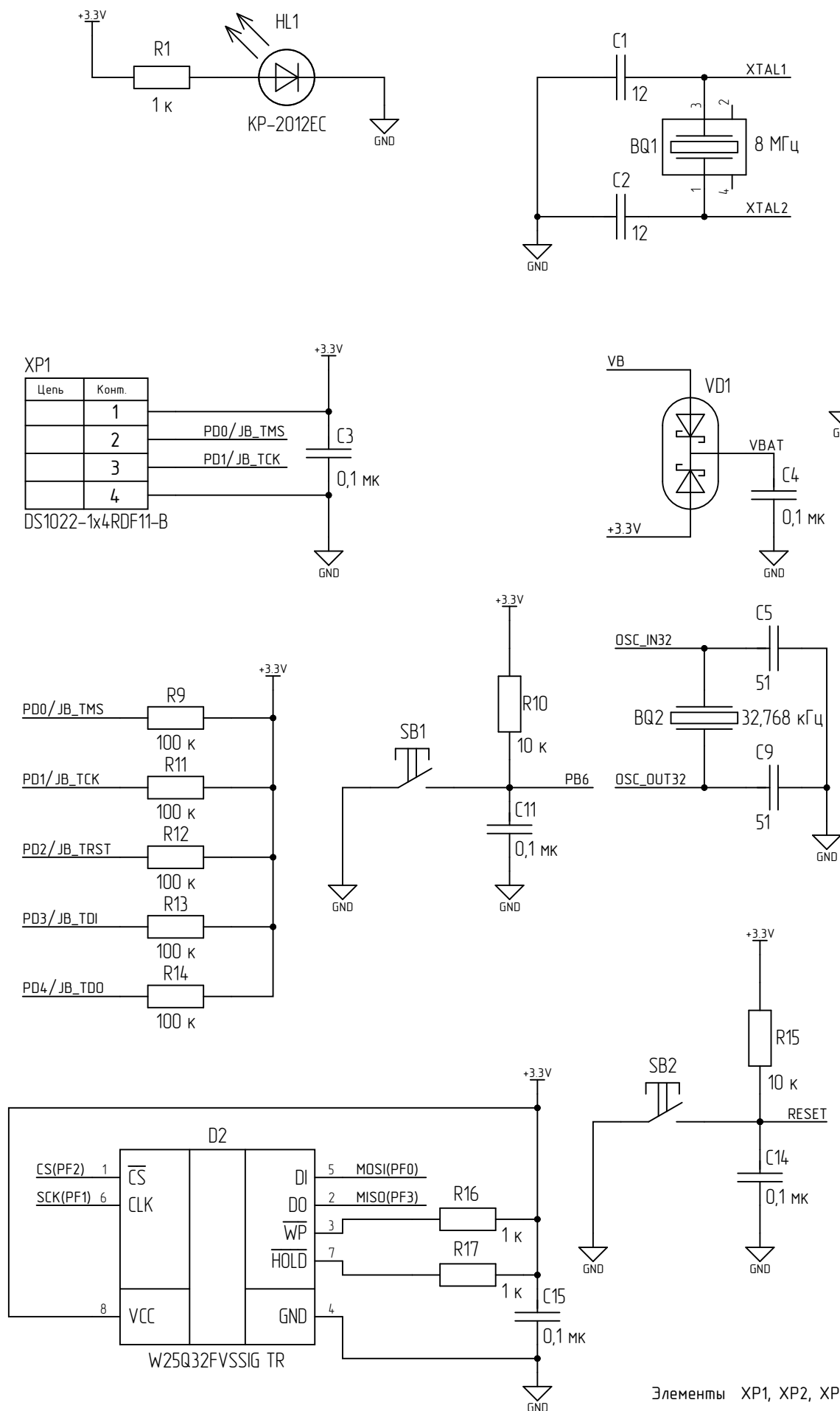




Элементы XP1, XP2, XP4, D2 не устанавливать.

PA0	6
PA1	6
PA2	6
PA3	6
PA4	5
PA5	5
PA6	5
PA7	5

PB0	4
PB1	4
PB2	4
PB3	4
PB4	4
PB5	5
PB6	5
PB7	5
PB8	5
PB9	5
PB10	5

SCL/PC0	4
SDA/PC1	4
PC2	4

XTAL2	1
XTAL1	2

RESET	1
WAKEUP	1

Timing diagram showing VBAT and MK signals. VBAT is high for 1,12,38.4 and low for 2. MK is high for 2 and low for 2.

A diagram of a ladder with 2 rungs. It consists of two vertical lines representing the sides and two horizontal lines representing the rungs. The rungs are labeled '2' on the right side.

PA0
PA1
PA2
PA3
PA4
PA5
PA6
PA7

	PB0/JA_TDO
	PB1/JA_TMS
	PB2/JA_TCK
	PB3/JA_TDI
	PB4/JA_TRST
	PB5
	PB6
	PB7
	PB8
	PB9
	PB10

PC0
PC1
PC2

OSC_IN	
OSC_OUT	
RESET	
WAKEUP	

Ucc
AUcc
AUcc1
BUcc

	AGND1
	AGND

PD0/JB_TMS
PD1/JB_TCK
PD2/JB_TRST
PD3/JB_TDI
PD4/JB_TDO
PD5
PD6
PD7

PE0
PE1
PE2
PE3
PE6
PE7

	PF0
	PF1
	PF2
	PF3
	PF4/MODE[0]
	PF5/MODE[1]
	PF6/MODE[2]

DN	
STANDBY	

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EPAD

31	PD0/JB_TMS
32	PD1/JB_TCK
33	PD2/JB_TRST
34	PD3/JB_TDI
30	PD4/JB_TDO
35	PD5
36	PD6
29	PD7

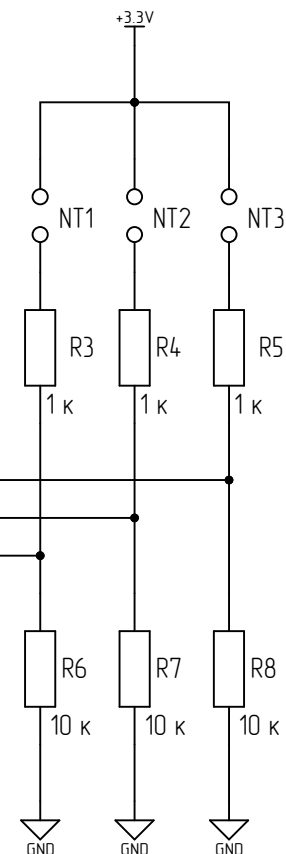
26	PE0
25	PE1
22	PE2
21	PE3
16	OSC_IN32
15	OSC_OUT32

2	MOSI(PF0)
3	SCK(PF1)
4	CS(PF2)
5	MISO(PF3)
6	PF4
7	PF5
8	PF6

Timing diagram showing USB_D_N and KT1 signals. USB_D_N is a square wave between 10 and 14. KT1 is a single pulse at 14.

4

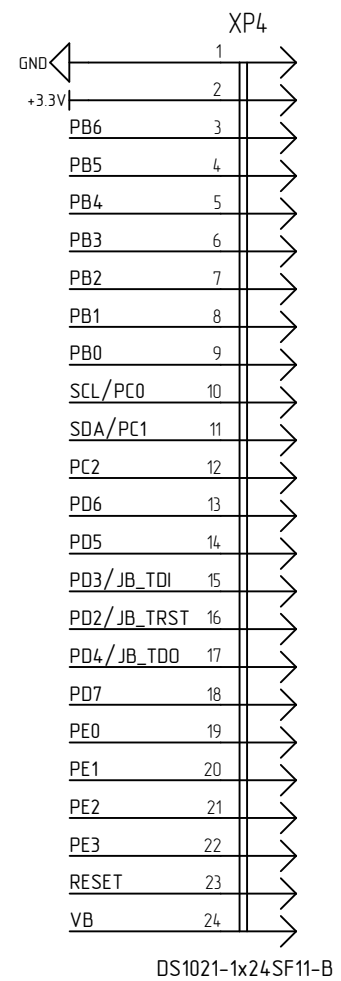
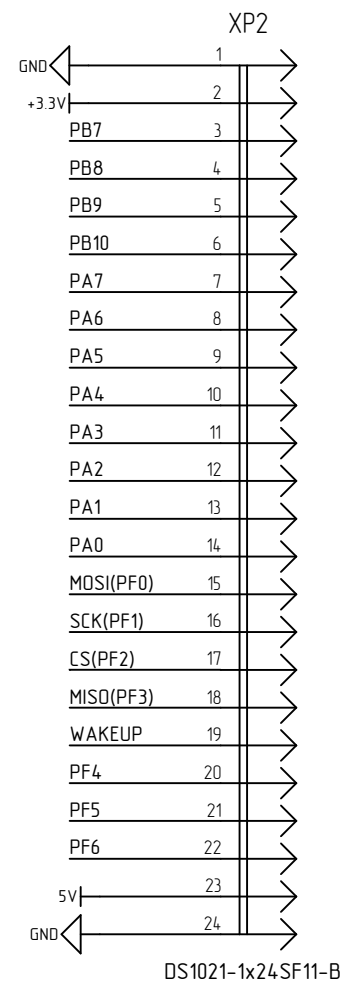
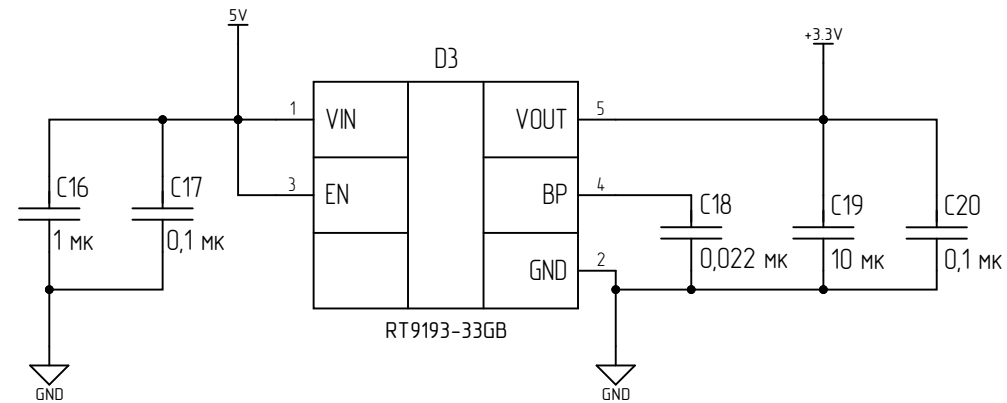
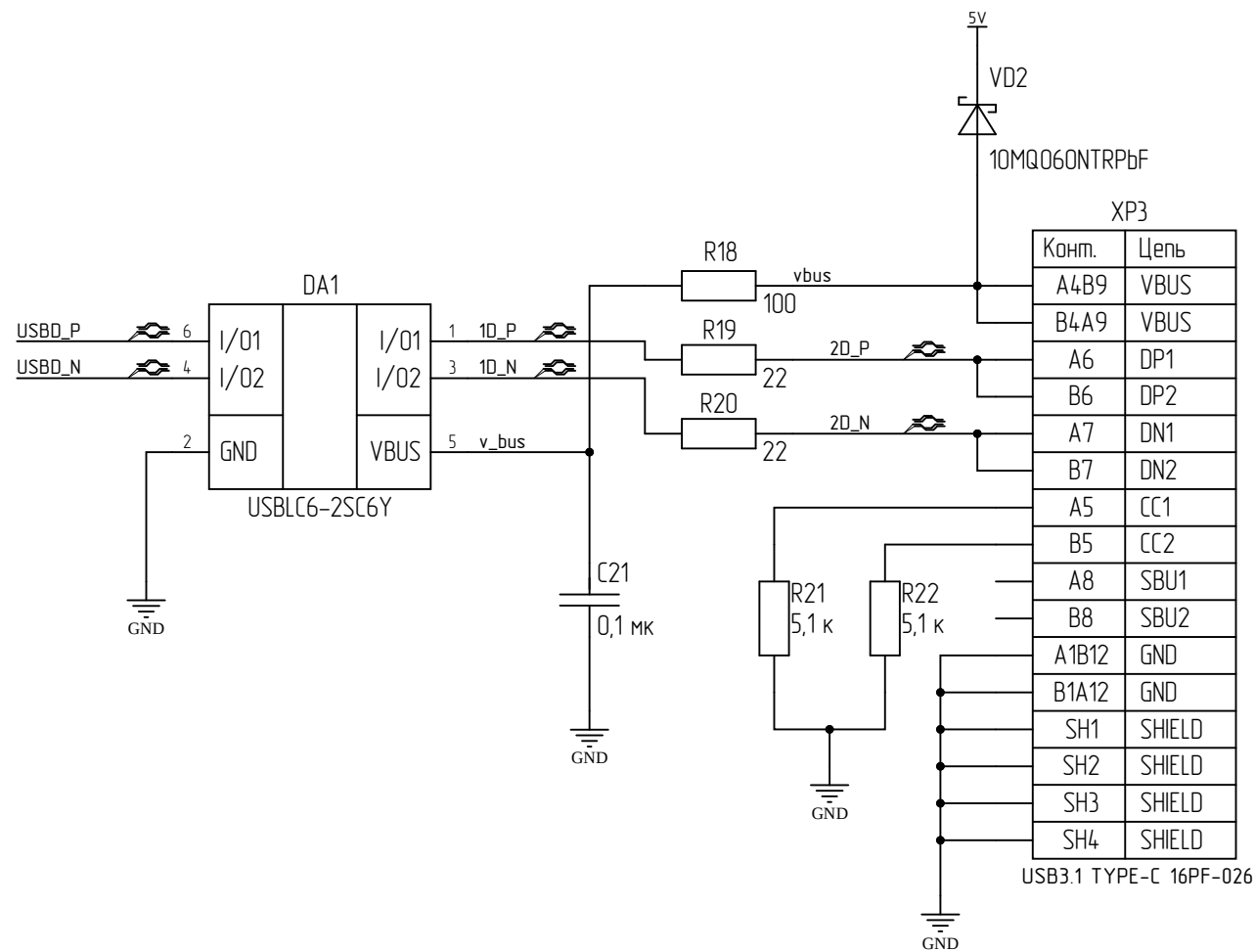
65  GND



Изм.	Лист	№ докум.	Подп.	Дат
Разраб.				
Пров.				
Т.контр.				
Н.контр.				
Утв.				

Лит.	Масса	Масштаб
Лист 1	Листов 2	
АО "ПСК Миландр"		

Инд. № подл.	Подп. и дата	Взам. инб. №	Инд. № дудл.	Подп. дата



Изм.	Лист	№ докум.	Подп.	Дата	Лист
					2